

DESCRIPTION

JW15156E is an isolated offline Flyback converter with GaN integrated, which features quasi-resonant (QR) operation. QR control improves efficiency by reducing switching loss and benefits EMI performance with nature frequency variation. And an internal frequency limitation is utilized to overcome the inherent disadvantages of QR Flyback.

JW15156E comprises a HV pin for startup to eliminate conventional startup resistor and save standby mode energy consumption.

JW15156E is available in PVDFN5*6-8 package. The high level of integration provides an easy-to-use, low component count and high efficiency application solution for isolated power delivery.

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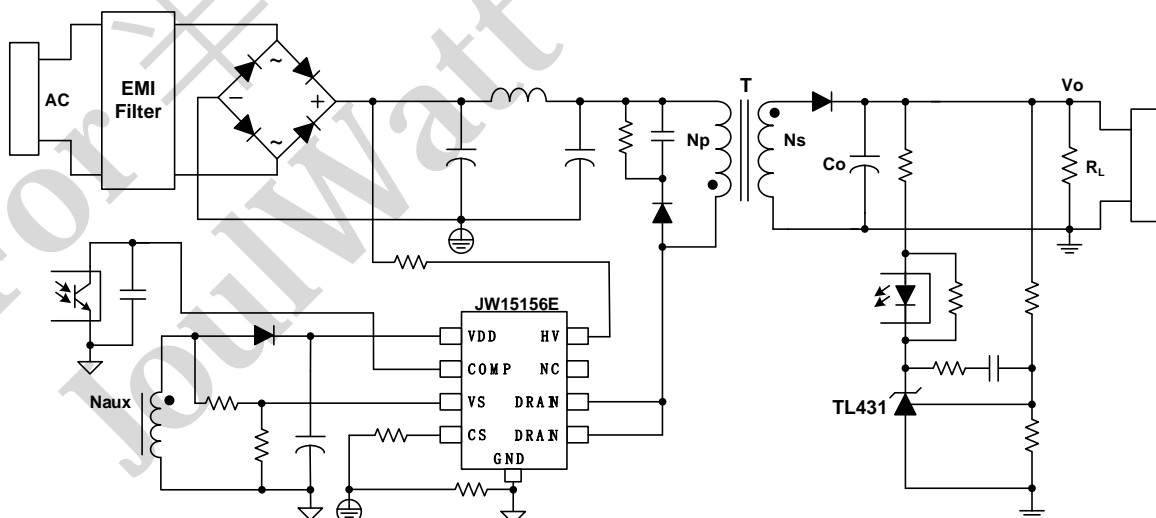
FEATURES

- Integrated 700V GaN
- Built-in High-Voltage Startup
- Wider VDD Operation Range
- QR Operation for High Efficiency
- Optional OCP and OPP Function for Different PD and QC Output Application
- Very Low Standby Power Consumption
- Cycle-by-Cycle Current Limit
- Reliable Fault Protections: VDD OVP, VS OVP, Brown-In, Brown-out, CS Open Protection, OCP, OPP, OLP, Internal OTP
- Frequency Jitter to Ease EMI Compliance
- Available in PVDFN5*6-8

APPLICATIONS

- PD and QC Chargers
- AC/DC Adapters with Wide Output Range

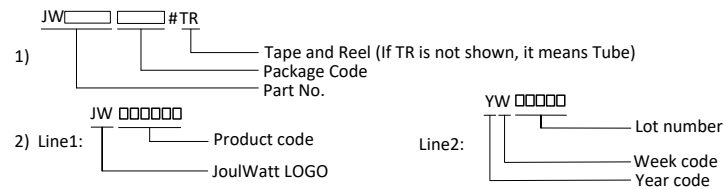
TYPICAL APPLICATION



ORDER INFORMATION

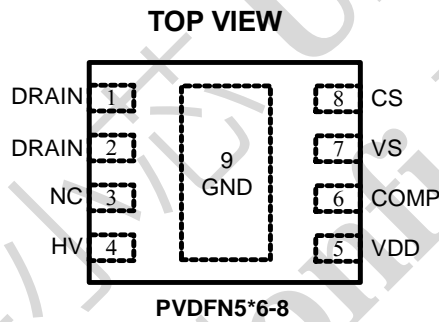
DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾	ENVIRONMENTAL ³⁾
JW15156EPVDLQF#TR	PVDFN5*6-8	JW15156E YW□□□□□	Green

Notes:



3) All JoulWatt products are packaged with Pb-free and Halogen-free materials and compliant to RoHS standards.

PIN CONFIGURATION



PIN DESCRIPTION

PIN PVDFN5*6-8	NAME	DESCRIPTION
1,2	DRAIN	Drain terminal of the Internal GaN.
3	NC	
4	HV	High voltage input pin. This pin provides source current to charge VDD. This pin also senses input voltage for brown-in and brown-out protection.
5	VDD	Bias power input to the converter. A hold-up capacitor to GND is required.
6	COMP	Feedback input pin for Flyback QR converter. Connect to an opto-coupler.
7	VS	Voltage sensing input pin. Coupled to the auxiliary winding via a resistor divider to monitor the output voltage for OVP protection. This pin also detects the resonant valley to implement QR operation.
8	CS	Current sensing input pin. This pin sense the primary switch current for peak current control and OCP. Besides, this pin is used to choose OCP or OPP function at the initial start.
9	GND	The ground of IC.

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